

Fig. 1a (Prior Art)

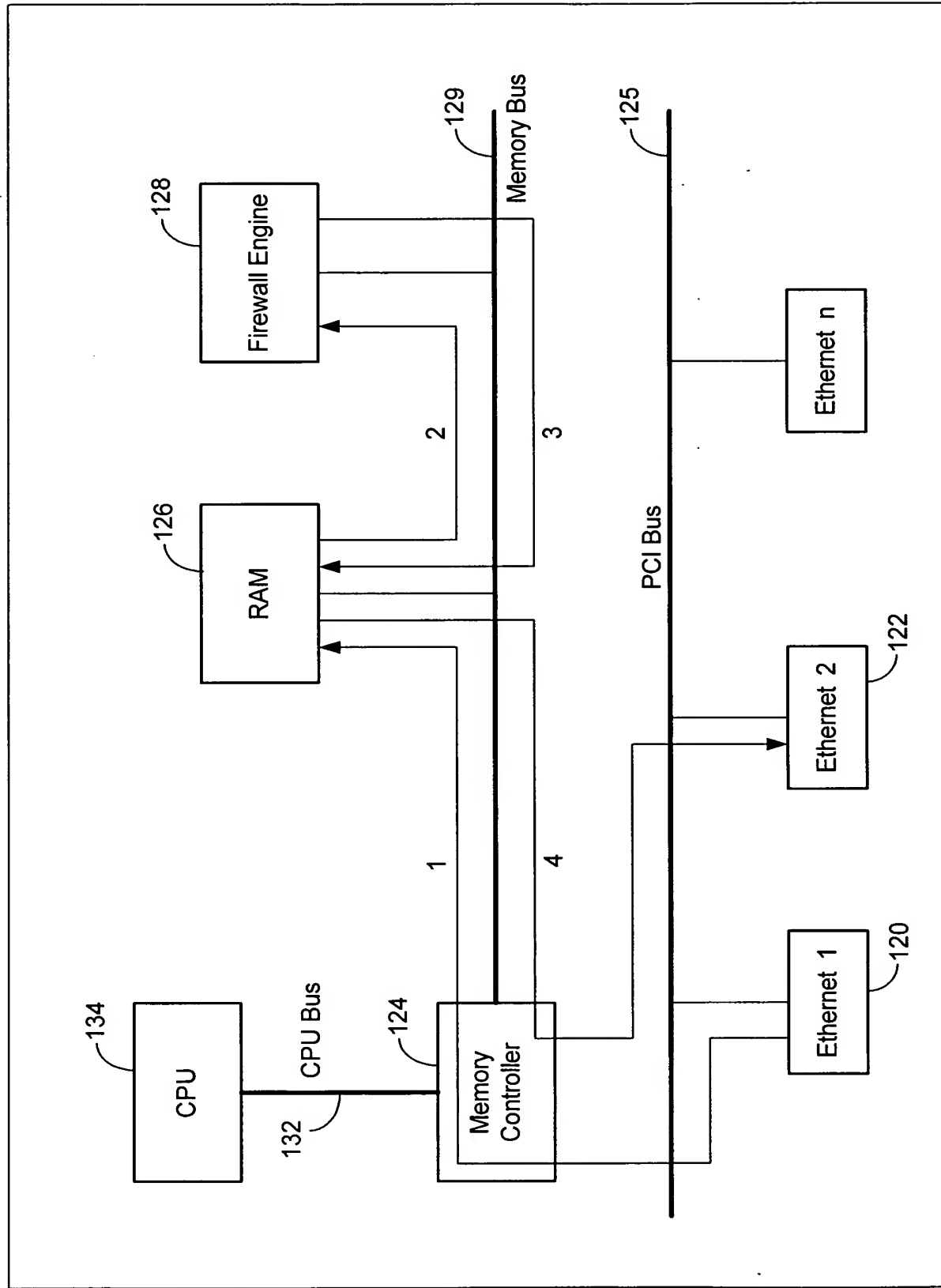


Fig. 1b (Prior Art)

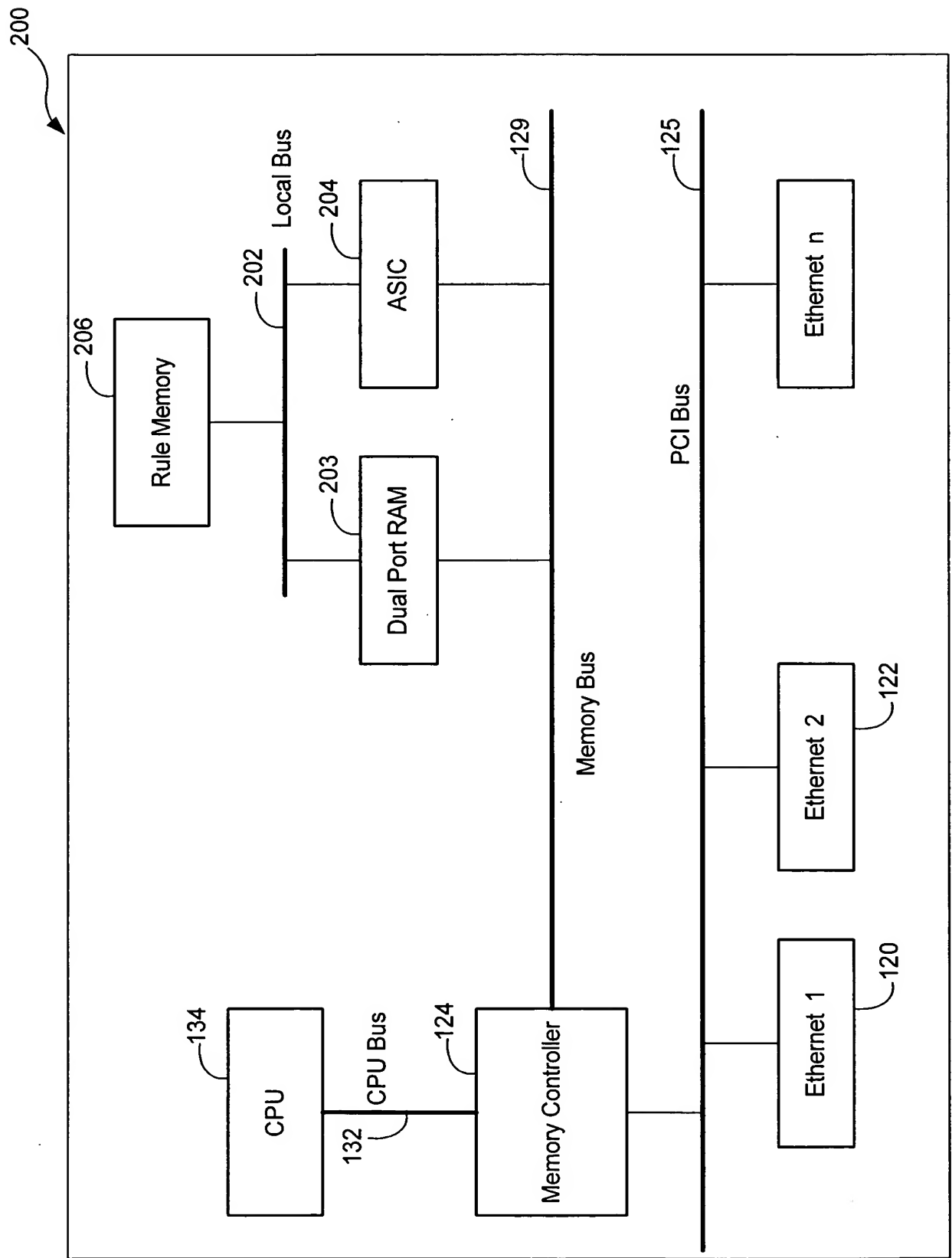


Fig. 2

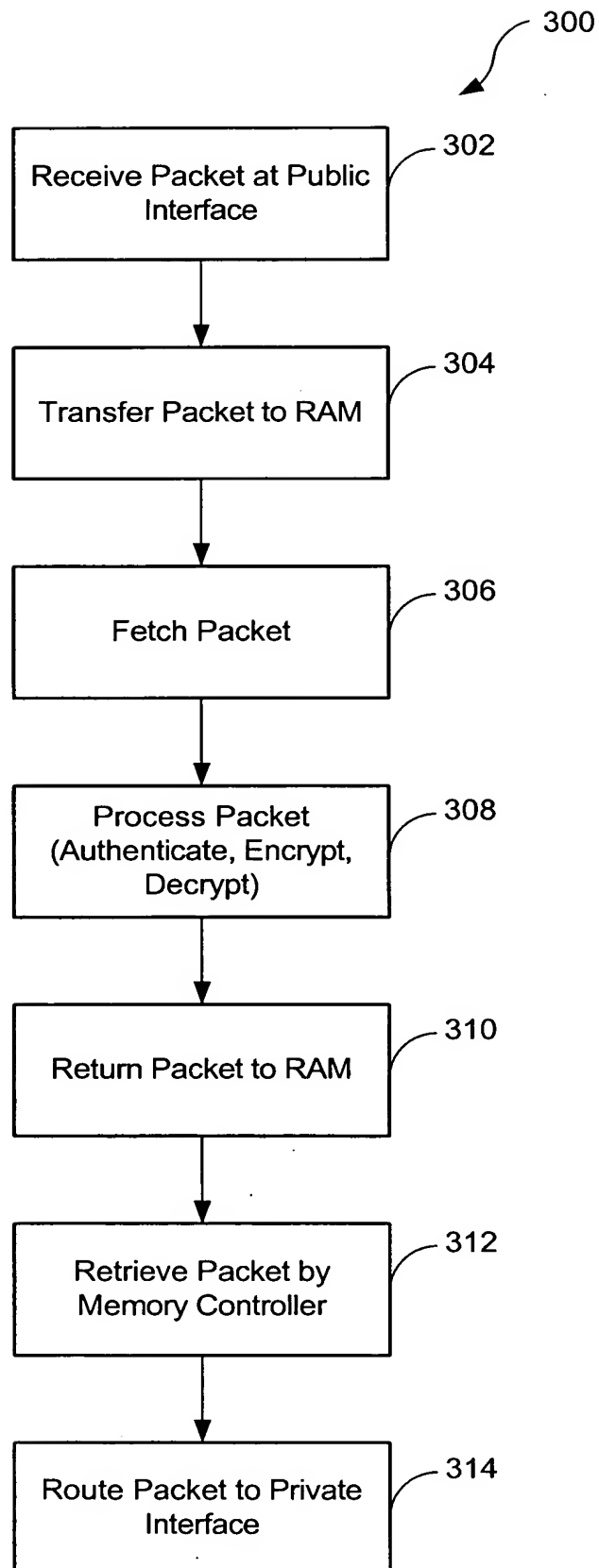


Fig. 3

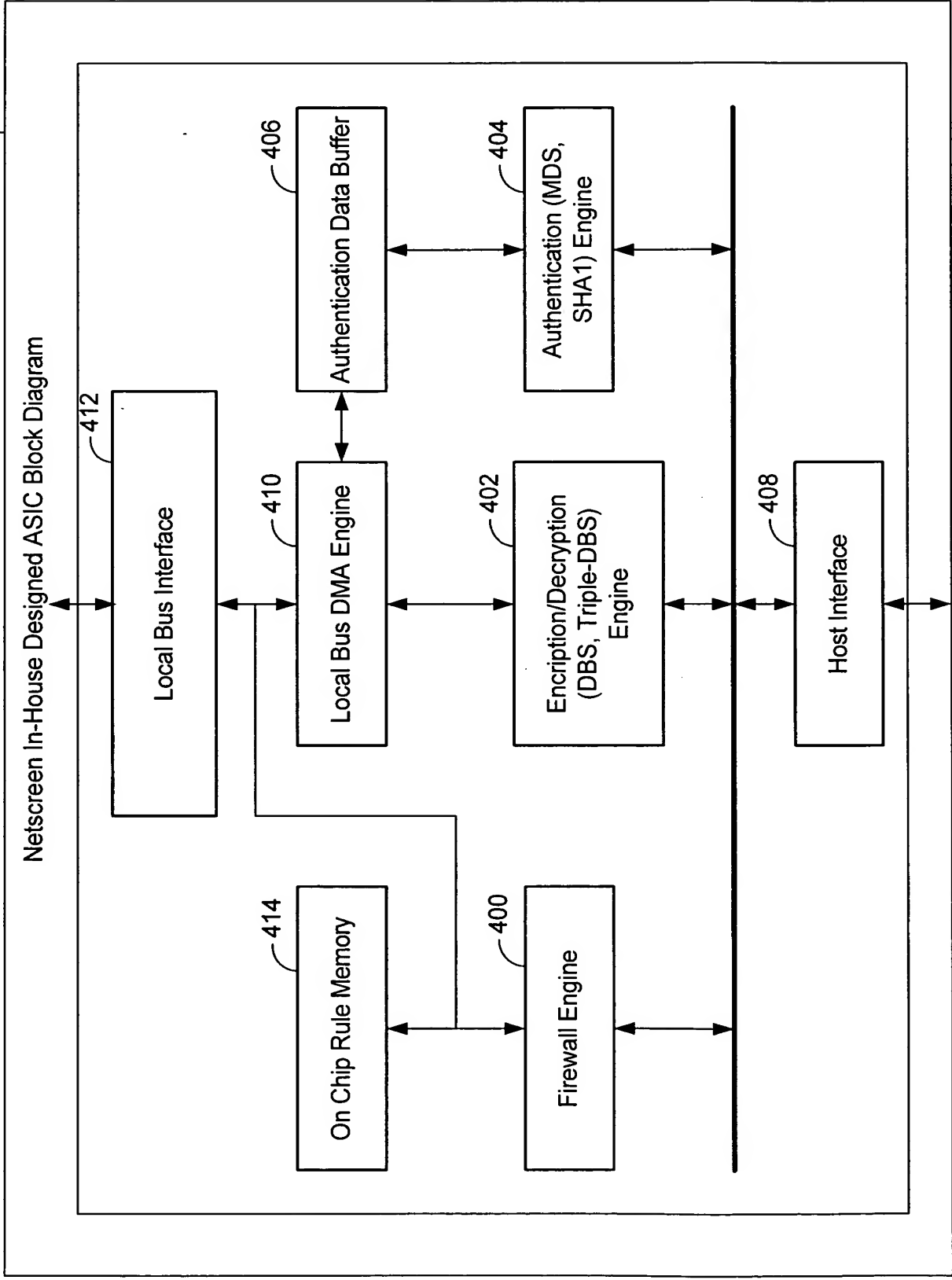


Fig.4

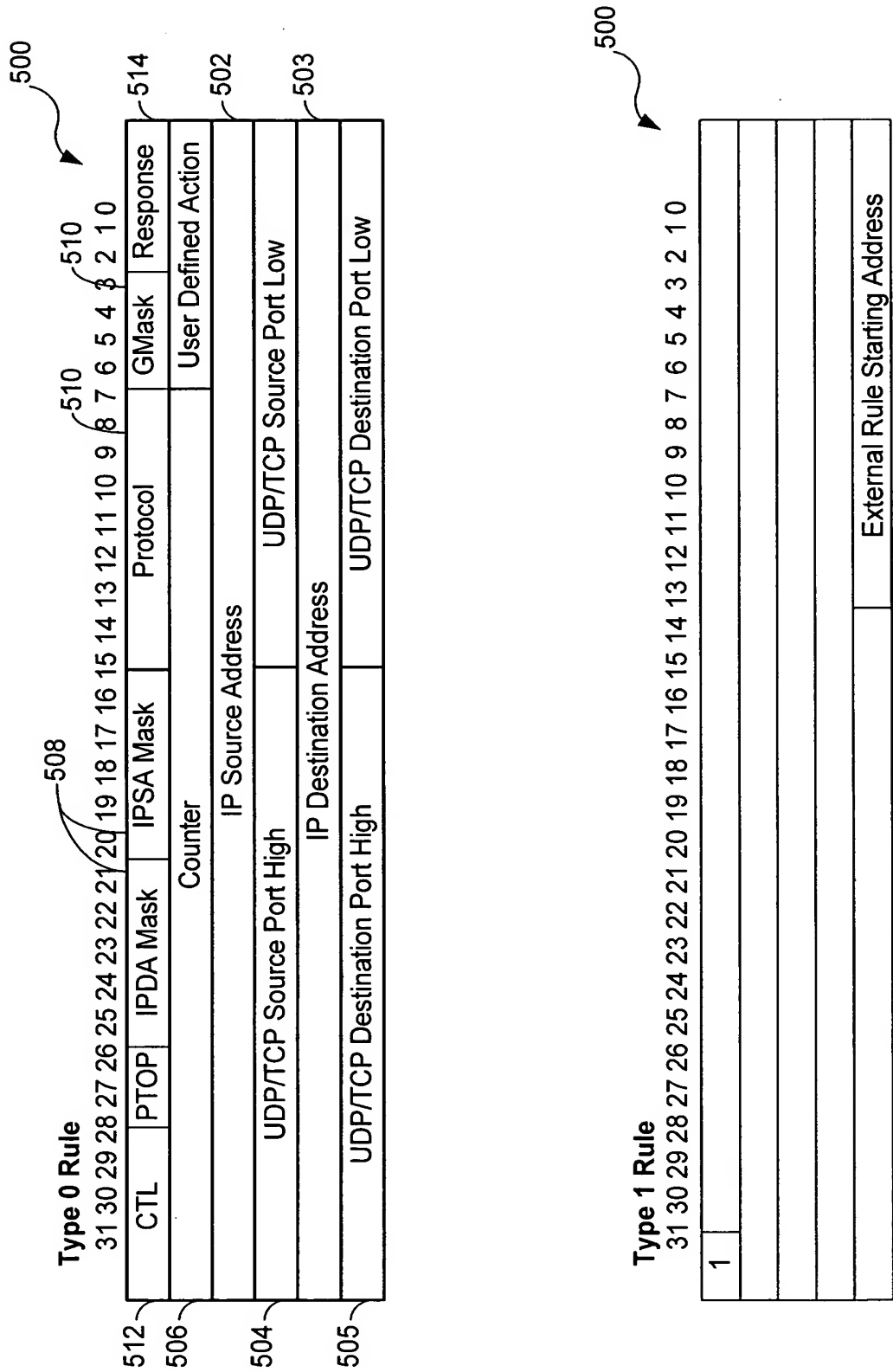
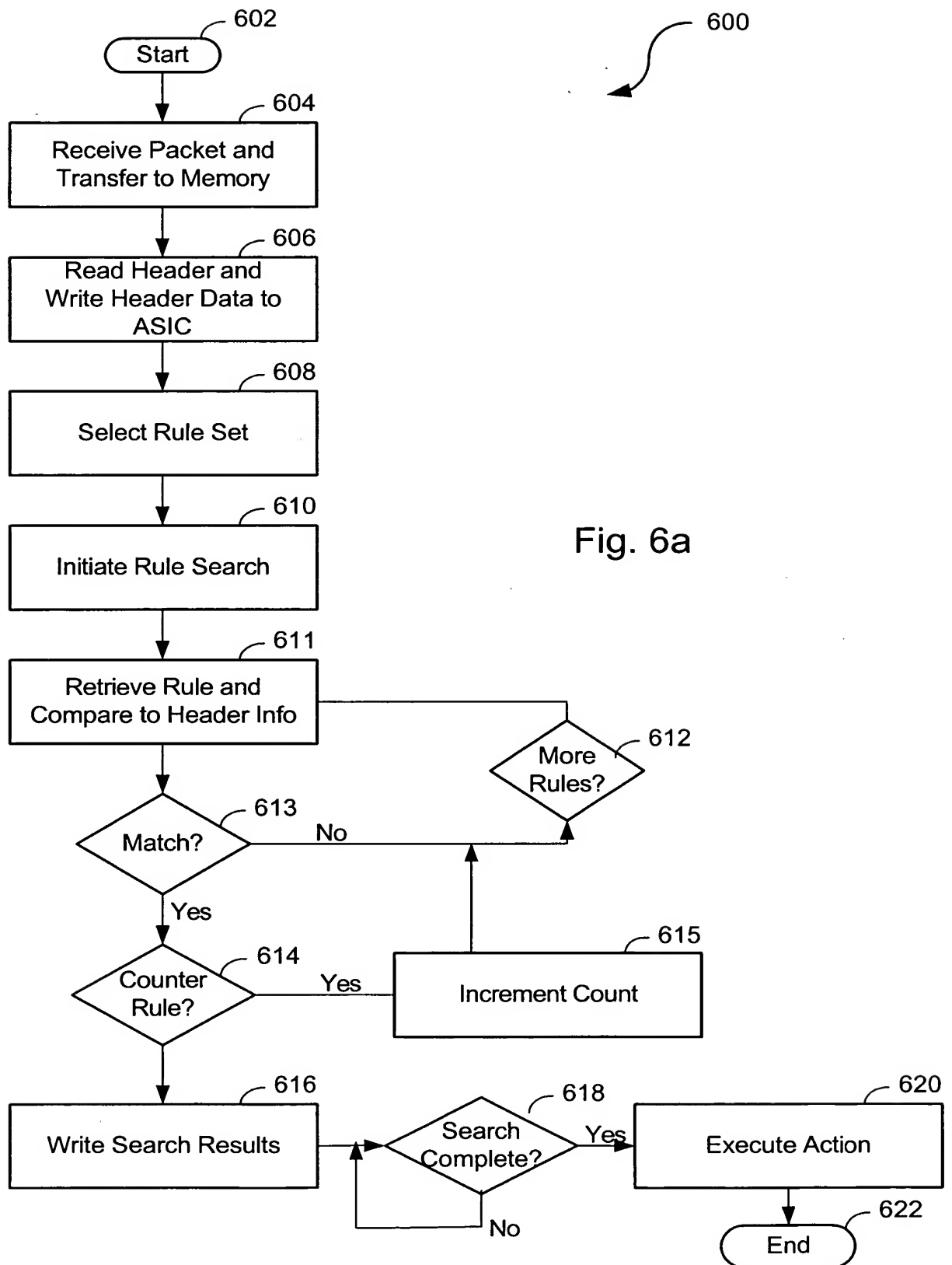


Fig. 5



	1 <sup>st</sup> Clock	2 <sup>nd</sup> Clock	3 <sup>rd</sup> Clock	4 <sup>th</sup> Clock	5 <sup>th</sup> Clock	6 <sup>th</sup> Clock	7 <sup>th</sup> Clock
1 <sup>st</sup> Rule	Fetch Rule	IP Comparison	Port Comparison				
2 <sup>nd</sup> Rule		Fetch Rule	IP Comparison	Port Comparison			
3 <sup>rd</sup> Rule			Fetch Rule	IP Comparison	Port Comparison		
4 <sup>th</sup> Rule				Fetch Rule	IP Comparison	Port Comparison	
5 <sup>th</sup> Rule					Fetch Rule	IP Comparison	Port Comparison

Fig. 6b



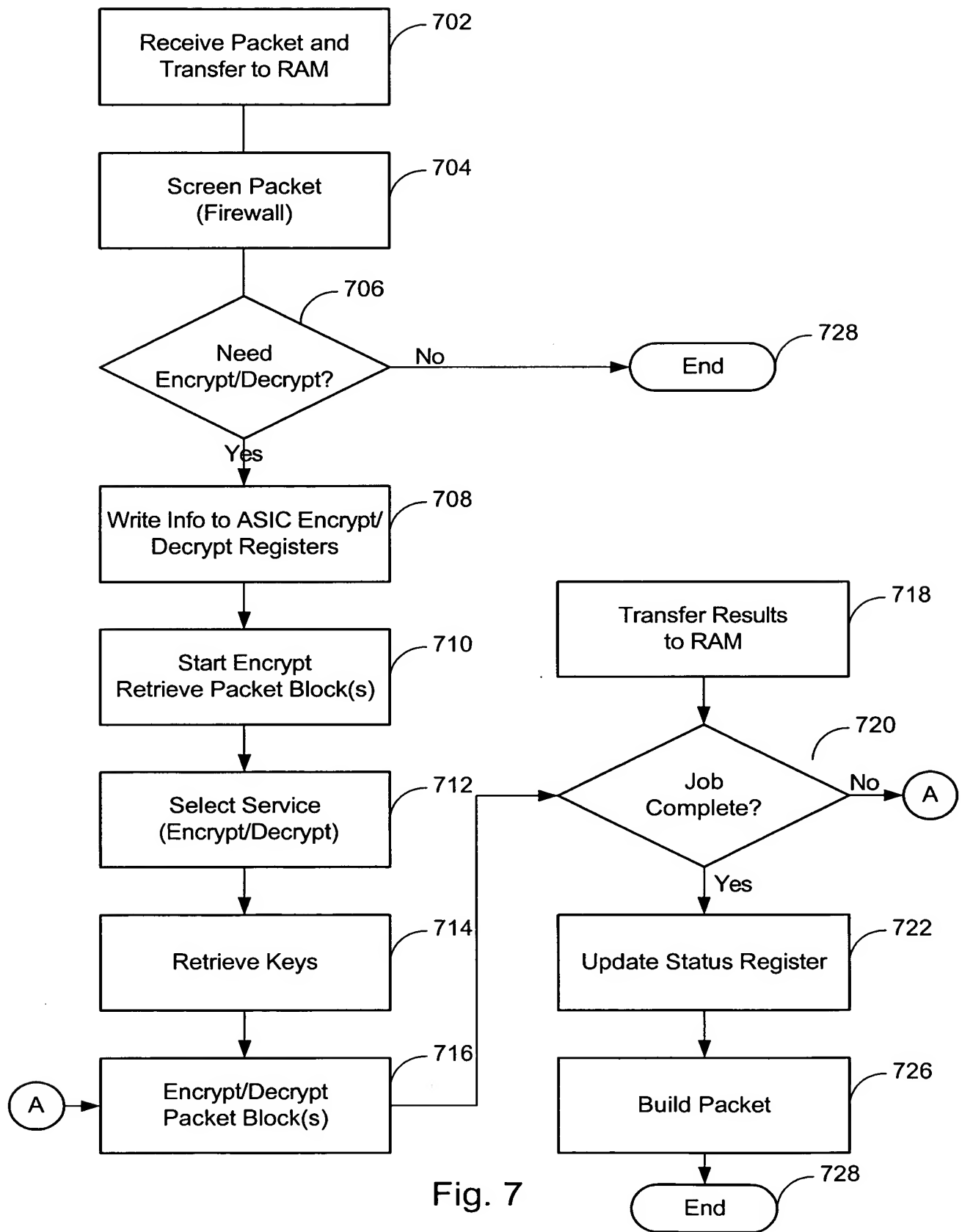


Fig. 7

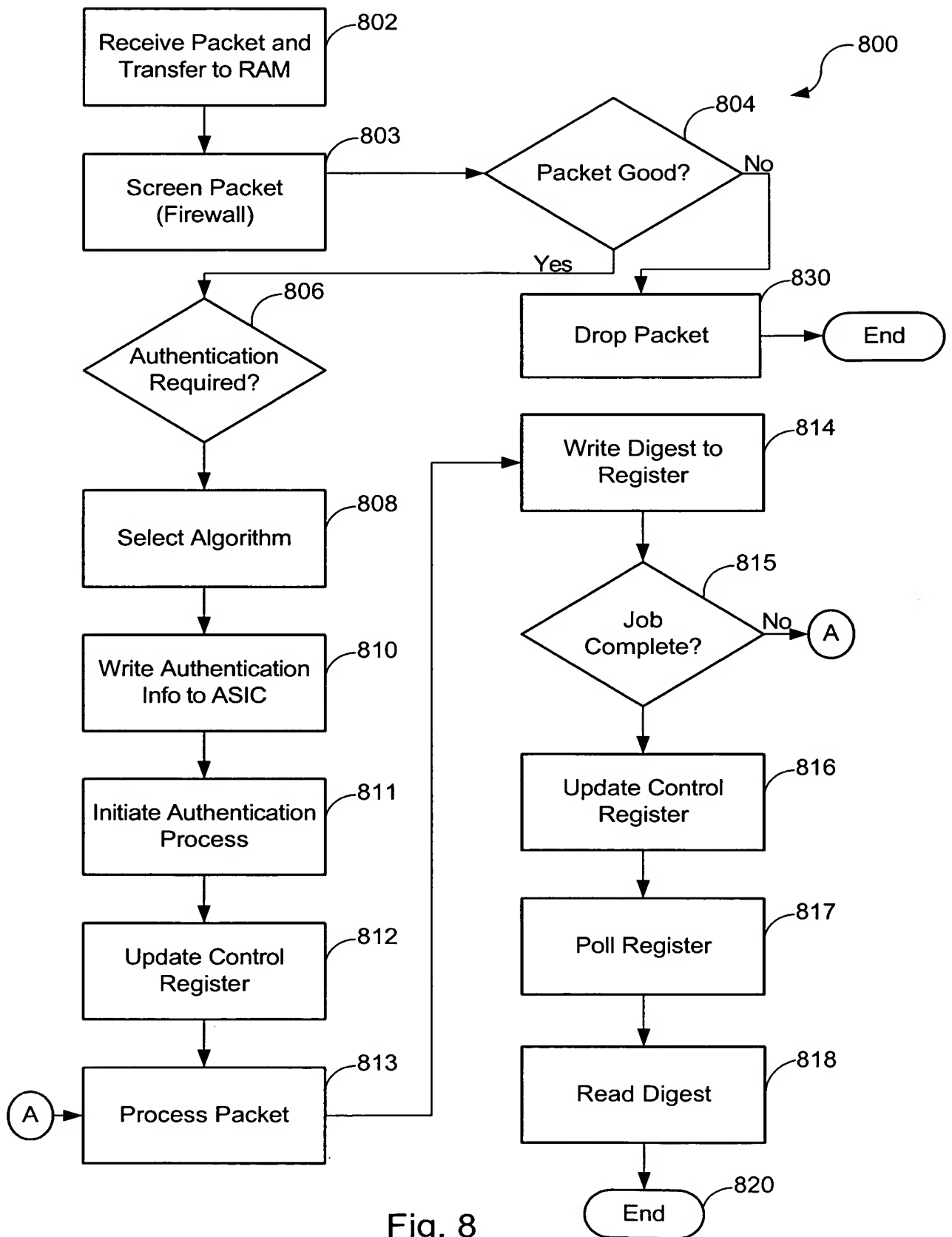


Fig. 8